IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Masayuki Sakakura et al.	Art Unit	: 2879
Serial No.	: 10/827,444	Examiner	: Ashok Patel
Filed	: April 20, 2004	Conf. No.	: 2785
Title	: DISPLAY DEVICE		

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO ELECTION REQUIREMENT

In response to the action mailed **May 17, 2006**, please amend the application as noted.

Amendments to the Claims

This listing of claims replaces all prior versions and listings of claims in the application.

Listing of Claims

1. **(Original)** A display device comprising:

a video signal line;

a current supply line arranged in parallel with the video signal line; and

an insulating layer between the video signal line and the current supply line,

wherein:

the video signal line is overlapped with the current supply line at least partly.

2. **(Original)** The display device according to claim 1, wherein a pixel electrode is formed on the same layer as the video signal line or the current supply line.

3. **(Withdrawn)** A display device comprising:

a video signal line;

a current supply line arranged in parallel with the video signal line;

a third line arranged in parallel with the current supply line; and

an insulating layer between the third line and one of the video signal line and the current supply line,

wherein:

the third line overlaps the one of the video signal line and the current supply line at least partly.

4. **(Withdrawn)** The display device according to claim 3, wherein a pixel electrode is formed on the same layer as at least one of the video signal line, the current supply line and the third line.

5. **(Currently Amended)** A display device comprising:

a video signal line;

a current supply line arranged in parallel with the video signal line;

a ~~third~~ power supply line arranged in parallel with the current supply line; and

~~a first~~ an insulating layer between the video signal line and the current supply line, and between the power supply line and the current supply line~~; and~~

~~a second insulating layer between the third line and one of the video signal line and the current supply line~~,

wherein:

the video signal line overlaps the current supply line at least partly, and

the ~~third~~ power supply line overlaps ~~the one of the video signal line and~~ the current supply line at least partly.

6. (**Original**) The display device according to claim 5, wherein a pixel electrode is formed on the same layer as at least one of the video signal line, the current supply line and the third line.

7. (**Currently Amended**) A display device comprising:

a first video signal line;

a second video signal line arranged in parallel with the first video signal line;

a current supply line arranged in parallel with the first video signal line;

an insulating layer between the first video signal line and the current supply line;

a switching transistor electrically connected to the first video signal line;

a driving transistor electrically connected to the switching transistor and the current supply line;

an erasing transistor electrically connected to the driving transistor and the current supply line; and

a light emitting device electrically connected to the driving transistor,

wherein:

the second video signal line overlaps the current supply line at least partly.

8. **(Original)** The display device according to claim 7, wherein a pixel electrode is formed on the same layer as at least one of the video signal line and the current supply line.

9. **(Withdrawn)** A display device comprising:

a video signal line;

a current supply line arranged in parallel with the video signal line;

a third line arranged in parallel with the current supply line;

an insulating layer between the third line and one of the video signal line and the current supply line;

a switching transistor electrically connected to the video signal line;

a driving transistor electrically connected to the switching transistor and the current supply line;

an erasing transistor electrically connected to the driving transistor and the current supply line; and

a light emitting device electrically connected to the driving transistor,

wherein:

the third line overlaps the one of the video signal line and the current supply line at least partly.

10. **(Withdrawn)** The display device according to claim 9, wherein a pixel electrode is formed on the same layer as at least one of the video signal line, the current supply line and the third line .

11. **(Currently Amended)** A display device comprising:

a first video signal line;

a second video signal line arranged in parallel with the first video signal line;

a current supply line arranged in parallel with the first video signal line;

a power supply line arranged in parallel with the current supply line;

an insulating layer between the first video signal line and the current supply line, and between the power supply line and the current supply line;

a switching transistor electrically connected to the first video signal line;

a driving transistor electrically connected to the switching transistor and the current supply line;

an erasing transistor electrically connected to the driving transistor and the current supply line; ~~and~~

~~a current control transistor electrically connected in series to the erasing transistor;~~

a light emitting device electrically connected to the driving transistor; and

a current control transistor electrically connected to the power supply line, and to the driving transistor in series between the current supply line and the light emitting device,

wherein:

the second video signal line overlaps the current supply line at least partly, and

the power supply line overlaps the current supply line at least partly.

12. (**Original**) The display device according to claim 11, wherein a pixel electrode is formed on the same layer as at least one of the video signal line and the current supply line.

13. (**New**) The display device according to claim 11,

wherein a channel length of the current control transistor is L_1,

wherein a channel width of the current control transistor is W_1,

wherein a channel length of the driving transistor is L_2,

wherein a channel width of the driving transistor is W_2, and

wherein L_1/W_1 is larger than L_2/W_2.

REMARKS

Claims 1-13 are pending, with claims 1, 3, 5, 7, 9 and 11 being independent. Claims 3, 4, 9 and 10 have been withdrawn, leaving claims 1, 2, 5-8 and 11-13 for prosecution with claims 1, 5, 7 and 11 being independent. Claims 5, 7 and 11 have been amended and new claim 13, which depends from claim 11, has been added. Support for the amendments and the new claim may be found in the application at, for example, Figs. 1 and 4, and page 10, lines 28-30. No new matter has been introduced.

In response to the action mailed May 17, 2006, applicant elects Species III, which includes Figures 4 and 11. Claims 1, 2, 5-8 and 11-13 are readable on this feature. The election is made without traverse.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

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